

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,847,252 B1  
DATED : January 25, 2005  
INVENTOR(S) : Ono et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

Item [54], Title, delete "SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE"  
and insert -- WELL BIAS VOLTAGE CONTROL CIRCUIT AND METHOD --

Signed and Sealed this

Twenty-eighth Day of June, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*